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SJ

**INDUSTRY STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

SJ/Z 11361-2006

**Integrated circuit intellectual property core protection:
schemes, alternatives and discussion**

集成电路IP核保护大纲

(English Translation)

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What this standard covers

This standard provides several techniques and mechanisms. This specification is intended to determine the open, interoperable IP protection solutions, including IP protection IP users should be weighed against the safety and protection of the availability of the relationship between the levels. IP (including products, technologies and software) through patents, copyrights and trade secrets be protected. For hard-core, solid core and soft-core IP, you can use the above method to select the appropriate protection mechanisms. This specification is intended for use including IP providers, IP users (system designer or integrator), EDA developers, in the FPGA, CPLD, ASIC or SoC used in semiconductor supplier to the IP. This specification can be given authorization to another party a variety of IP protection, detection and tracking mechanisms. This specification does not involve IP to be used in the design process to the design process (EDA tools) protection.

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