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**INDUSTRY STANDARD OF THE
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**Integrated circuit soft and hard IP core structural, performance
and physical modeling specification**

集成电路IP软核、硬核的结构、性能和物理建模规范

(English Translation)

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What this standard covers

This standard specifies the IC IP core (Intellectual Property Core, hereinafter referred to as IP)-based data representation format to support from the register transfer level (RTL, Register Transfer Level) design and planning to the final hardware design verification process. This specification includes the following: IP and IP soft-core hard core RTL source code and its required performance model format summary of the requirements on the design constraints. This specification covers the structural netlist formats and data used in some hard-core IP-related physical data types. The specification also includes a set of guiding principles to reduce between different IP, IP, and other system logic on a namespace between the potential for conflict, as well as a namespace conflict to some of the principles. The specification of the data format of the "applications" is defined as the creation of the integrated circuit IP, definition, exchange and integration of description.

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