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CCS L 56

SJ

**INDUSTRY STANDARD OF THE
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On-Chip bus attributes specification

片上总线属性规范

(English Translation)

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What this standard covers

This standard applies to systems integrators, IC IP (Intellectual Property Core, hereinafter referred to as IP) developers and bus developers using or developing on-chip bus is used when the on-chip bus stop supporting material and cultural preparation. According to the definition in this specification, can be divided into on-chip bus system bus and peripheral bus.

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