

ICS 31.200
CCS L 56

SJ

**INDUSTRY STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

SJ/Z 11355-2006

Specification for integrated circuit IP/SoC functional verification

集成电路IP/SoC功能验证规范

(English Translation)

Issued on: September 26, 2006

Implemented on: December 1, 2006

Issued by: Ministry of Industry and Information Technology

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What this standard covers

This standard applies to end-users in their product integration and chip-level verification process reusable IP vendors to deliver verification environment. Typically, the verification environment by the test platform, model, script files, and other deliverables project components, this specification shall be described in detail. This specification does not give the various functions on the authentication method to make a specific explanation, does not require all of the methods used in the verification. In the use of a method, should refer to the corresponding provisions of this specification of the method described.

Document data

International classification (ICS): 31.200.

Chinese classification (CCS): L56.

Estimated length of the Chinese original: 55,558 words.