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SJ

**INDUSTRY STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

SJ/Z 11354-2006

Integrated circuit analog/mixed-signal IP core specification

集成电路模拟/混合信号IP核规范

(English Translation)

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What this standard covers

This standard specifies the deliverables and associated design guidelines and data formats, in order to determine the process has been mixed signal IP cores (Intellectual Property Core, hereinafter referred to as IP) for testing, exchange and integration. These components are primarily digital system chip applications as the target. This specification also provides a detailed list of deliverables model, in order to determine the craft has been mixed signal IP for exchange, integration and verification. Data Format "field of use" is defined as the creation of an integrated circuit IP, definition, exchange and integration of description.

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