

ICS 31.200
CCS L 56

SJ

**INDUSTRY STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

SJ/T 12038-2025

**A method for verifying the consistency of analog chip designs
(English Translation)**

Issued on: September 9, 2025

Implemented on: March 1, 2026

Issued by: Ministry of Industry and Information Technology

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This standard specifies the verification method for the design consistency of analog chips. It applies to the evaluation and verification of design consistency for intended analog chips by chip users (such as equipment companies and system companies) or by chip companies for the chips they develop.

Document data

International classification (ICS): 31.200.

Chinese classification (CCS): L56.

Estimated length of the Chinese original: 27,213 words.