

**INDUSTRY STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

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Electronic design hardware description language VHDL

电子设计硬件描述语言VHDL

(English Translation)

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This standard specifies the electronic design automation and manufacturing process electronic systems and circuits, especially digital integrated circuit hardware description and design language (VHDL) procedures. This standard applies to electronics, computer classes, and communications electronics design, including electronic information systems, electronic circuits and all kinds of electronic products, especially digital integrated design. When using the electronic design automation (EDA) tools for the design, where it is used in the following criteria: a. Digital IC design simulation, synthesis, verification and testing, b. For system-level printed circuit board (PCB) level, chip-level, register transfer level and gate level description of all kinds of complex electronic design, c. electronic circuits and electronic systems behavioral, structural, and data flow description: d. EDA tool design among designers occasionally data exchange.

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