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**NATIONAL STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

GB/T 7497.3-2008

Replacing GB/T 7497.3-1987

**Microprocessor system bus. 8-bit and 16-bit data (MULTIBUS ?)
-- Part 3: Mechanical and pin descriptions for the Eurocard
configuration with pin and socket (indirect) connectors**

微处理器系统总线 8位及16位数据(MULTIBUS I) 第3部分: 对带有插针与插座连接器(间接配合)的欧洲板配置的机械与插针的描述

(English Translation)

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Foreword

GB/T 7497 "microprocessor system bus 8 and 16-bit data (MULTIBUSI)" is divided into three sections.

- Part 1. Electrical and timing specifications functional description;
- Part 2. The description of the system bus with edge connectors (direct) configuration of mechanical and pins;
- Part 3. Mechanical and pin descriptions for the Eurocard configuration with pin and socket connectors (indirect) configuration.

This part of GB/T 37497 Part of, identical with IEC 60796-3:1990 "Microprocessor system bus 8 and 16 Data (MULTIBUSI) - Part 3. European board with pin and socket (indirect) mechanical and pin configuration description of".

This section also IEC 60796-3:1990 made the following editorial changes.

- Remove the IEC 60791-3 "preface";
- Increase the national standard foreword.

This Part replaces GB/T 7497.3-1987 "Microprocessor system bus 1 8 bit and 16-bit data - Part 3. configuration with pin with the receptacle connector (indirect) system bus configuration Mechanical and pin descriptions. " this part of the GB/T 7497.3-1987 made the following main changes.

--- By the name of the third part of "Microprocessor system bus 1 8 bit and 16-bit data. The pin and socket connector (indirect distribution Co) illustration Mechanical and pin system bus configuration "with" Microprocessor system bus 8 and 16-bit data (MULTIBUSI) - Part 3. European board with pin and socket (indirect) and mechanical configuration Pin description ";

Text --- text changes.

This part of the National Information Technology Standardization Technical Committee and centralized.

This section drafted by. China Ministry of Electronics Industry Standardization Institute.

Introduction

GB/T 7497 consists of three parts. a functional description and two are available mechanical standards. This standard allows a variety of micro-processing System components interact with each other in the electrical and mechanical interface. Parallel transmission channel and application interface bus is used as the tight coupling between the system components Interconnection number.

GB/T 7497.3-2008/IEC 60796-3:1990 Microprocessor system bus 8 and 16-bit data (MULTIBUSI) Part 3. with pin and socket connectors (Indirect) European plate configuration Describing mechanical and pin

1 Scope

This section GB/T 7497 applies to the aid of the pin socket (indirect) backplane connector type to connect the microprocessor system Interface Unit.

Objective 2

This section GB/T 7497 is intended to ensure the three bus backplane card cage and the printed circuit board is mechanically compatible provided information.

3 Definitions

The following terms and definitions apply GB/T 7497 of this section.

One kind of loaded 96 pin connector for a circuit board. This circuit board interconnects with some pins on the connector to provide a bus.

3.1.1

Europe will be in the bottom plate inserted into the connector side.

Insert the bottom plate with a similar installed on the same backplane printed circuit board other board communications.

3.2.1

Containing components in the European side of the board.

3.2.2

In Europe with the board edge connector with the circuit board connectors on the backplane.

4 The European board connector Europe board requires a 96 pin connector of IEC 603-2 (in accordance with IEC Publication 130-14), mounted on the top plate of Europe Rear edge and named P1; if the connector is mounted on the rear edge of the plate at the bottom of Europe, is named P2, and should be connected to a standard IEC Connector.

Europe should not be in the front panel Component height exceeds 15.3mm (0.6in).

Female connector mounted on the backplane, male connector mounted on the module.

GB/T 7497.3-2008/IEC 60796-3:1990