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**NATIONAL STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

GB/T 4937.39-2025

**Semiconductor devices - Mechanical and climatic test methods - Part
39: Measurement of moisture diffusivity and water solubility in organic
materials used for semiconductor components**

半导体器件 机械和气候试验方法 第39部分：半导体器件用有机材料的潮气扩散
率和水溶解度测量

(English Translation)

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Table of Contents

Preface
Introduction
1 Scope
2 Normative References
3 Terms and Definitions
4 Instruments
5 Samples
6 Program
6.1 Sample Preparation
6.2 Moisture absorption measurement below 100°C
6.3 Calculation of water solubility and diffusivity
6.4 Moisture absorption measurement above 100°C
7 Calculation of activation energy for moisture diffusion
8 Calculation of the water solubility fitting function
9 Explanation
References

Foreword

This document complies with the provisions of GB/T 1.1-2020 "Standardization Work Guidelines Part 1: Structure and Drafting Rules of Standardization Documents".

Drafting.

This document is Part 39 of GB/T 4937, "Mechanical and Climatic Testing Methods for Semiconductor Devices". GB/T 4937 has been published.

The following section.

---Part 1: General Provisions;

---Part 2: Low Pressure;

---Part 3: External Visual Inspection;

---Part 4: Highly Accelerated Steady-State Damp Heat Test (HAST);

---Part 8: Sealing;

- Part 10: Mechanical Impact Devices and Components;
- Part 11: Rapid Temperature Change in Two-Liquid Tank Method;
- Part 12: Sweep Frequency Vibration;
- Part 13: Salt Spray;
- Part 14: Lead Strength (Lead Wire Secureness);
- Part 15: Solder Heat Resistance of Through-Hole Mounting Devices;
- Part 16: Particle Collision Noise Detection (PIND);
- Part 17: Neutron Irradiation;
- Part 18: Ionizing Radiation (Total Dose);
- Part 19: Chip Shear Strength;
- Part 20: Combined effects of moisture and soldering heat on molded surface mount devices;
- Part 20-1. Handling, Packaging, Marking, and Shipping of Surface Mount Devices Sensitive to the Combined Effects of Moisture and Solder Heat;
- Part 21: Solderability;
- Part 22: Bond Strength;
- Part 23: High-Temperature Operating Life;
- Part 24: Accelerated Moisture Resistance Test with Unbiased Strong Accelerated Stress;
- Part 25: Temperature Cycling;
- Part 26: Electrostatic Discharge (ESD) Sensitivity Testing Human Model (HBM);
- Part 27: Electrostatic Discharge (ESD) Sensitivity Testing Machine Model (MM);
- Part 29: Latch Test;
- Part 30: Pretreatment of Unsealed Surface Mount Devices Prior to Reliability Testing;
- Part 31: Flammability of Molded Devices (Internal Causes);
- Part 32: Flammability of Molded Devices (Externally Caused);
- Part 33: Accelerated Moisture-Resistant, Non-Biased High-Pressure Cooking;
- Part 34: Power Cycling;
- Part 35: Acoustic Microscopy Inspection of Plastic-Encapsulated Electronic Components;
- Part 36: Steady-state acceleration;

- Part 37: Plate Drop Test Method Using Accelerometers;
- Part 38: Soft Fault Testing Methods for Semiconductor Devices with Storage;
- Part 39: Measurement of moisture diffusivity and water solubility of organic materials for semiconductor devices;
- Part 40: Plate Drop Test Method Using Strain Gauges;
- Part 42: Temperature and Humidity Storage;
- Part 44: Test methods for single-event effects (SEE) by neutron irradiation on semiconductor devices.

This document is equivalent to IEC 60749-39:2021 "Mechanical and climatic testing methods for semiconductor devices - Part 39: Semiconductor devices" Measured using the moisture diffusivity and water solubility of organic materials.

The following minimal editorial changes have been made to this document.

---The content regarding the URL of the ISO/IEC terminology database in Chapter 3, "Terminology and Definitions," has been removed.

Please note that some content in this document may involve patents. The issuing organization of this document assumes no responsibility for identifying patents.

This document was proposed by the Ministry of Industry and Information Technology of the People's Republic of China.

This document is under the jurisdiction of the National Semiconductor Device Standardization Technical Committee (SAC/TC78).

Introduction

Semiconductor devices are general-purpose basic products in the electronics industry chain and are the most basic units in electronic systems. GB/T 4937 Semiconductor the series of standards for mechanical and climatic testing methods for semiconductor devices are fundamental and universal standards for testing semiconductor devices, and are essential for evaluation and assessment.

The quality and reliability of nuclear semiconductor devices play a crucial role. It is planned to consist of 44 parts.

Semiconductor devices are general-purpose basic products in the electronics industry chain and are the most basic units in electronic systems. GB/T 4937 "Semiconductor Devices" the "Mechanical and Climate Test Methods for Semiconductor Devices" is a fundamental and universal standard for testing semiconductor devices, used for evaluating and assessing semiconductors.

The device plays a crucial role in quality and reliability and is planned to consist of 44 parts.

---Part 1: General Principles. The purpose is to establish general guidelines for mechanical and climatic testing methods for semiconductor devices.

---Part 2: Low Pressure. The purpose is to test the ability of components and materials to avoid electrical breakdown failure.

---Part 3: External Visual Inspection. The purpose is to inspect whether the materials, design, structure, markings, and manufacturing processes of semiconductor devices meet the required standards.

Requirements for procurement documents.

---Part 4: Strong Accelerated Steady-State Damp Heat Test (HAST). The purpose is to specify the Strong Accelerated Steady-State Damp Heat Test (HAST) to detect...

To test the reliability of non-hermetically sealed semiconductor devices in humid environments.

---Part 5: Steady-State Temperature and Humidity Offset Lifetime Test. The purpose is to specify the steady-state temperature and humidity offset lifetime test to detect non-hermetic leaks.

Reliability of packaged semiconductor devices in humid environments.

---Part 6: High-Temperature Storage. The purpose is to investigate the effects of high-temperature storage on semiconductor devices without applying electrical stress.

---Part 7: Internal Moisture Measurement and Other Residual Gas Analysis. The purpose is to assess the quality of the packaging process and provide information on the gas content.

Information on the long-term chemical stability of the substance within the shell.

---Part 8: Sealing. The purpose is to detect the leakage rate of semiconductor devices.

---Part 9: Marking Durability. The purpose is to test the durability of markings on semiconductor devices.

---Part 10: Mechanical Shock Devices and Components. The purpose is to test semiconductor devices and printed circuit board assemblies against moderate stress.

The ability to adapt to shocks.

---Part 11: Rapid Temperature Change Two-Batter Method. The purpose is to define the rapid temperature change (two-batter method) for semiconductor devices.

The test procedures, failure criteria, and other related content.

---Part 12: Sweep Frequency Vibration. The purpose is to detect the effect of vibration on semiconductor devices within a specified frequency range.