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**NATIONAL STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

GB/T 47030-2026

**Model and specification series of electric double-layer
supercapacitor cells**

双电层单体超级电容器型号规格系列

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Foreword

This document complies with the provisions of GB/T 1.1-2020 "Standardization Work Guidelines Part

1.Structure and Drafting Rules of Standardization Documents". Drafting. Please note that some content in this document may involve patents. The issuing organization of this document assumes no responsibility for identifying patents. This document was proposed and is under the jurisdiction of the Ministry of Industry and Information Technology of the People's Republic of China. This document was drafted by: China Electronics Technology Standardization Institute, Nantong Jianghai Energy Storage Technology Co., Ltd., and Fujian Torch Electronics Technology Co., Ltd. Limited Liability Company, Xi'an Thermal Power Research Institute Co., Ltd., Shanghai Aowei Technology Development Co., Ltd., China National Petroleum Corporation Liuhe Electronics (Jiangxi) Co., Ltd., and Guangdong Jiewei Electronics Co., Ltd. The main drafters of this document are. Cao Jinzhu, Yang Endong, Ding Jiajia, Lin Junhong, Wu Pengyue, Wang Kaixing, Kang Zhen, An Zhongxun, Fu Yiqin, and Cao Lin. Cai Xueyun, Ding Mingjun, Yao Jianjun. Double-layer single-cell supercapacitor model and specification series

1 Scope

GB/T 47030-2026 is the Chinese national standard covering the designation and preferred sizes of supercapacitor cells - the capacitance and voltage series, the case dimensions and terminals, so that a cell from one maker can be designed into a module that will take another's. First edition, with GB/T 47029-2026 on electrode testing. It was issued on 28 January 2026 and has been in force since 1 August 2026, as a first edition. The document is under the responsibility of the Ministry of Industry and Information Technology. This page is published from the official record of the 2026 edition; the clause text of a standard this recent is not yet in circulation, and the figures, limits and tables it contains are those of the document itself, delivered in full with the English translation.

This document defines the terminology and definitions for double-layer supercapacitors, provides classifications, and specifies model and specification series. This document applies to double-layer supercapacitors (hereinafter referred to as double-layer capacitors). It also applies to pseudocapacitive single-cell supercapacitors and hybrid supercapacitors. This applies to other types of single-cell supercapacitors, such as single-cell supercapacitors and battery-type single-cell supercapacitors.

2 Normative references

The contents of the following documents, through normative references within the text, constitute essential provisions of this document. Dated citations are not included. For references to documents, only the version corresponding to that date applies to this document; for undated references, the latest version (including all amendments) applies. This document. SJ/T 11625-2016 Classification and Model Naming Method for Supercapacitors SJ/T 11861-2024 Terminology for Supercapacitors

3 Terms and Definitions

The terms and definitions defined in SJ/T 11861-2024, as well as the following terms and definitions, apply to this document.

3.1 Supercapacitors primarily store energy through the double-layer capacitance formed at the electrode/electrolyte interface.

3.2 Double-layer capacitors with button-type housings and corresponding pin configurations.

3.3 A double-layer capacitor with a cylindrical housing and corresponding pin configuration.

4 Product Classification, Models, and Specifications

4.1 Product Classification According to their product shape and structure, double-layer capacitors are mainly divided into button-type double-layer capacitors and cylindrical double-layer capacitors. Model

4.2 According to SJ/T 11625-2016, the model of a double-layer capacitor consists of four parts. identification, type, rated voltage, and rated capacity.

4.3 Specifications The specifications of a double-layer capacitor consist of its size and lead configuration.

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