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Requirements for wafer carrier handoff parallel I/O interface

晶片承载器传输并行接口要求

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Foreword

This document is in accordance with the provisions of GB/T 1.1-2020 "Guidelines for standardization work Part 1: Structure and drafting rules for standardization documents" Drafting.

Please note that some of the contents of this document may involve patents. The issuing organization of this document does not assume the responsibility for identifying patents.

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Wafer carrier transmission parallel interface requirements

1 Scope

This document specifies the signal definitions, signal transmission requirements, exception handling, and Connector and sensor requirements.

This document applies to the field of semiconductor manufacturing and is used for the automatic transfer of wafer carriers between active and passive equipment. operate.

2 Normative references

GB/T 15125-1994

3 Terms and definitions

The following terms and definitions apply to this document.

3.1 Wafer carrier wafercarrier

Wafer racks, cassettes, boats and other carriers for storing wafers.

3.2 Storage stack stocker

A storage device used to temporarily store wafer carriers or wafers in a semiconductor production line.

3.3 Small car

A kind of automatic transporter with vertical lifting and transfer device suspended under the aerial transmission track in the semiconductor production line.

3.4 Overhead shuttle

A semiconductor production line is installed on an aerial transmission track, using a transfer robot to move the wafer carrier between devices.

Automatic transport vehicle for transmission.

3.5 Rail-guided vehicle

A semiconductor production line that runs on a ground transfer track and uses a transfer robot to transfer wafer carriers between devices. Automatic transport vehicle.

3.6

A semiconductor production line that runs on the ground without mechanical guidance and uses a transfer robot to move the wafer carrier between devices.