ICS 31.200
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NATIONAL STANDARD OF THE PEOPLE'S REPUBLIC OF CHINA

GB/T 43455-2023

Analog/mixed-signal intellectural property(IP)core quality evaluation

模拟/混合信号知识产权（IP）核质量评测

Issued on: December 28, 2023 **Implemented on: April 1, 2024**

Issued by: State Administration for Market Regulation;
Standardization Administration of the PRC

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Foreword

This document complies with the provisions of GB/T 1:1-2020 "Standardization Work Guidelines Part 1: Structure and Drafting Rules of Standardization Documents" Drafting:

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This document is under the jurisdiction of the National Semiconductor Device Standardization Technical Committee (SAC/TC78):

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Analog/Mixed-Signal Intellectual Property (IP) Core Quality Assessment

1 Scope

This document specifies the evaluation content of analog/mixed-signal intellectual property (IP) core quality, including the documentation quality of the IP core, the electrical quality of the IP core Circuit design quality, physical design quality, model quality, IP core function verification quality and IP core tape-out verification quality, etc:

This document is suitable for providers, users and third parties of analog/mixed-signal IP

cores to evaluate the quality of analog/mixed-signal IP cores, including It includes completeness and reusability, and does not cover the evaluation of specific functions and performance:

2 Normative reference documents

GB/T 43452-2023

GB/T 43453-2023

3 Terms and definitions

The following terms and definitions apply to this document: 3:1 Components that are defined in advance, verified, reusable, and capable of completing certain functions:

Core, solid core and hard core: [Source: GB/T 43452-2023,3:1] 3:2 IP core providerIPcoreprovider The entity that creates and provides IP cores during the IP core transaction process:

Note: IP core providers will provide IP core related information and services: [Source: GB/T 43452-2023,3:2] 3:3 IP core user IP core user The entity that receives the IP core during the IP core transaction process:

Note: IP core users will complete the integration and reuse of IP cores, corresponding to IP core providers: [Source: GB/T 43452-2023,3:3] 3:4 Mandatory Deliverables that must be submitted: Note: The code is M:

[Source: SJ/T 11477-2014,2:3, modified]