

ICS 29.020
CCS K 04



**NATIONAL STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

GB/T 20295-2014

Replacing GB/T 20295-2006

Application of symbols for binary logic and analogue elements

二进制逻辑元件和模拟元件符号的应用

(English Translation)

Issued on: September 30, 2014

Implemented on: April 1, 2015

Issued by: Standardization Administration of China

Table of Contents

Introduction

1 Scope

2 Normative references

The common rule of 3 white box and gray box symbols

3.1 General

3.2 Terminal notation

3.3 Power Code

3.4 Non-logical representation of

3.5 Common control box, common output element

3.6 Embedded symbol

3.7 Use internal connection symbol

3.8 Read Only Memory (ROMs), programmable read-only memories (PROMs), and erasable programmable read only memories (EPROMs)

3.9 Input and output sequence

The order of 3.10 element

3.11 Total qualifying symbol position

3.12 Hysteresis Symbol

3.13 Amplification symbols and binary logic element

3.14 Symbol outside the box code of order

3.15 Scan function

3.16 Internal bus connection

3.17 Lower power symbols

Foreword

This standard was drafted in accordance with GB/T 1.1-2009 given rules.

This standard is equivalent to using IEC TR61734:2006 "binary logic elements, and analog component symbols of applications", and made editorial modify.

This standard replaces GB/T 20295-2006 "GB/T 4728.12 and GB/T 4728.13 standard application." this standard compared with GB/T 20295-2006 main changes are as follows.

--- Standard name to the IEC 61734:2006 the same name. "binary logic elements, and analog component symbols of application";

- Full of symbols of the new formulation increases GB/T 4728 way of expression;
- Increasing the reference standard GB/T 20296-2012 "law and integrated circuit memory symbol";
- In Chapter 3, we added the sentence. use-related memory function and method of input or output and focused on defining symbols GB/T 20296 provides.
- Original 11 Table 1 instead of the original images are adjusted accordingly Figure 11 after a sequence number order.

This standard by the National Electrical information structures, documentation and graphical symbols Standardization Technical Committee (SAC/TC27) and focal points.

This standard drafting units. China Aerospace Science and Industry Corporation II Institute 23, China Aerospace Science and Industry Corporation II Institute, Hefei Switzerland Qi Information Technology Co., Ltd., machine Productivity Promotion Center.

1 Scope

This standard specifies the application of GB/T 4728.12 and GB/T 4728.13 draw graphical symbols rules that allow a graphic symbol prepared Cause, and can accurately represent the function. This standard has also developed 4728.13 does not include the additional provisions in GB/T 4728.12 and GB/T , its inclusion was recommended to GB/T 4728.12 and GB/T 4728.13, and to draw symbols to help.

This standard applies to the field of design and simulation application binary logic elements element symbol.

GB/T 4728.12 and GB/T 4728.13 were provided for in Rule binary logic elements, and analog components of graphical symbols for Under different circumstances indicate feature provides an alternative method.

2 Normative references

The following documents for the application of this document is essential. For dated references, only the dated version suitable for use herein Member. For undated references, the latest edition (including any amendments) applies to this document.

GB/T 4728 (all parts), Graphical symbols for electrical diagrams (IEC 60617databaseIDT)
GB/T 20296 IC memory method symbol (IEC 61352:2006, IDT) IEEE1149.1 Standard
Access Port and Boundary-Scan Architecture
(StandardtestAccessportandboundary-scanarchi-tecture) the common rule of 3 white box and gray box symbols in the white box symbols, features a device has been fully described by way of a standardized method.

In the gray box symbols, it features by other methods (in part) Description (see GB/T

4728.12 symbol S01731 (12-54-01) and application notes A00037).

Using the method functions and associated memory and input or output and defining symbols concentrated in GB/T 20296 provides.

3.1 General

In binary logic elements and analog components - Graphic symbols Standard (GB/T 4728.12 and GB/T 4728.13) field by provisions the symbolic method, the same element can be expressed in different ways. In order to unify similar functions of notation, it is necessary to limit the different forms the number of symbols. In GB/T 4728, allowing both select one symbol, in this standard, preferably one of them.

This standard aims to unify drawn graphic symbols, it does not limit the freedom of a more appropriate representation needed in the circuit diagram, such as Exchange terminals.

3.2.1 NC terminals

NC terminals must not be connected to the terminal does not describe the function and do not have access, it is not represented on the symbol box.

3.2.2 Power supply terminal

Usually on a diagram describing the function, without showing the power supply terminal, so they are generally not represented in the symbol box, and otherwise at