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**Information technology -- High performance serial bus
(English Translation)**

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What this standard covers

This standard specifies high-speed, low-cost serial bus. It is suitable for use as a peripheral bus or parallel backplane bus spare bus. The salient features of this serial bus comprising: a) automatically assigned node address no address switch. b) on the ISDN compatible bit rate variable rate data transmission, the base plate from a TTL 24. 576Mbit/s backplane becomes the BTL 49. 152Mbit/s, and the cable media 98. 304Mbit/s, 196. 608Mbit/s and 393. 216Mbit/s. c) Cable Media allows up to 16 physical connection (cable hops), each hop up to 4. 5m, given any total cable distance between the two devices is 72m. Bus management to optimize the performance to identify a smaller configuration. d) Includes blocks and a single four -byte read and write, and to provide low overhead, a guaranteed bandwidth services " isochronous " mode bus transaction. e) Support cable media and backplane bus physical layer. f) to ensure equal access to all nodes in the fair bus access mechanism. Backplane environments add a priority mechanism, but it is not sure to use at least a fair agreement nodes still get the kind of unfair access mechanism. g) with ISO/IEC 13213: 1994 (IEEE std 1212-1991) coordinated.

Document data

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