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**NATIONAL STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

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**Integrated Circuit/Computer Hardware Description Language
Verilog**

集成电路/计算机硬件描述语言Verilog

(English Translation)

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Table of Contents

What this standard covers	1
Document data	2

What this standard covers

This standard applies to: Verilog hardware description language a complete specification. This document includes: Verilo, HDL all structures formal syntax and semantics, analog systems tasks and functions, such as text output display commands, compiler directive, such as text substitution macros and simulation the time scale, Programming language interface connecting mechanism, access to routine, routine task/function routines, and Verilog-based interface to process routine formal syntax and semantics, application examples, PLI header file list.

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