

ICS 35.100.10

CCS L 79



**NATIONAL STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

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**Information technology. High-Performance Parallel Interface.
Part 1: Mechanical, electrical and signaling protocol
specification (HIPPI-PH)**

信息技术 高性能并行接口 第1部分：机械、电气及信号协议规范(HIPPI-PH)
(English Translation)

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What this standard covers

This standard specifies the mechanical, electrical and signaling protocol specification for data processing equipment between the high-performance, point-to-point simplex interface. The interface described in this standard can 800Mbit/s or 1600Mbit/s peak data rate operations, the use of more than copper from 25m. Even if the transmission distance exceeds a predetermined HIPPH case, irrespective of the distance signal protocol also allows the average data rate close to the peak data rate. This standard applies to, through the physical layer and data layer at framing a common interface to provide for the development and application of computer systems. The interface on different computers, high-performance display systems and high-performance intelligent block transfer between peripheral devices to provide an effective interconnection. The interface for the larger block transfer is optimized.

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