

ICS 17.220
CCS L85



**NATIONAL STANDARD OF THE
PEOPLE'S REPUBLIC OF CHINA**

GB/T 15471-2013

Replacing GB/T 15471-1995

General specification for logic analyzer

逻辑分析仪通用规范

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Quarantine of the People's Republic of China;
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Table of Contents

- 1 Scope
- 2 Normative references
- 3 Terms and Definitions
- 3.2 Input
- 4 Products 7
- 6 Test methods
- 7 Inspection rules 24 8 signs, packaging, transportation and storage

Foreword

This standard was drafted in accordance with GB/T 1.1-2009 given rules. This standard replaces GB/T 15471-1995 "Logic Analyzer General technical requirements and test methods", this standard and GB/T 15471- Compared to 1995, the main changes are as follows:

--- An increase of some terms and definitions.

--- Increasing the threshold step, glitch pulse width, test methods trigger sequence speed and other parameters. Please note that some of the content of this document may involve patents. Release mechanism of the present document does not assume responsibility for the identification of these patents. The standard proposed by the Ministry of Industry and Information Technology of the People's Republic of China. This standard by the National Standardization Technical Committee Electronic Measuring Instruments (SAC/TC153) centralized. This standard was drafted. University of Electronic Science and Technology, Ministry of Industry and Information Technology Institute of Electronics Standardization. The main drafters of this standard. Hou Jun, Dai Zhijian, Shi Yi Bing, Huang Yinghua, Tian Shulin, Cao Ling. This standard replaces the standards previously issued as follows:

--- GB/T 15471-1995. Logic Analyzer General Specifications

1 Scope

GB/T 15471-2013 is the Chinese national standard on general specification for logic analyzer, in the field of metrology and measurement. The /T suffix marks it as a recommended standard: it is not compulsory by itself, but it becomes binding as soon as a contract, a tender or a customer specification calls it up - which in practice is how most foreign buyers meet it. It was issued on 17 December 2013 by the General Administration of Quality Supervision, Inspection and Quarantine of the People's Republic of China;

Standardization Administration of China, and has been in force since 15 May 2014.

Classification: ICS 17.220, CCS L85. This page is published from the official record of the standard held by the Chinese standards administration: the identification, the dates, the classification and the issuing body are taken from there. The clause text, the tables and the numeric limits are in the document itself, which is delivered complete in English translation.

This standard specifies the terms, product classifications, requirements, test methods, inspection rules, logic analyzer, signs, packaging, transportation and storage. This standard applies to a logic analyzer, also applies to the logical insertion unit or logic analyzer attachment of measuring instruments, external The logic analyzer can also refer to this standard.

2 Normative references

The following documents for the application of this document is essential For dated references, only the dated edition applies to this article Member. For undated references, the latest edition (including any amendments) applies to this document.

GB/T 191 Packaging - Pictorial signs

GB/T 3047.6 desktop chassis electronic equipment Series of basic dimensions

GB/T 6587-2012 General specification for electronic measuring instruments

GB/T 11463-1989 reliability tests for electronic measuring instruments EMC requirements for electrical equipment

GB/T 18268 (all parts) measurement, control and laboratory use

3 Terms and Definitions

The following terms and definitions apply to this document.

3.1 Logic Analyzer logic analyzer Being a trigger event relative to the digital logic signals, the signals in various ways to display and analyze the test equipment. Its main It can be characterized by a single channel (serial) or multi-channel (parallel) to record and display before and after the trigger event or trigger event capture To logic signals. Usually by the logic analyzer input, capture, storage, and trigger control, processing and other display components.

3.2 Input

3.2.1 Probe probe Soft cable connection, the signal can be transmitted to the instrument input devices.

3.2.2 Input impedance input impedance Logic analyzer under operating conditions presented to the circuit under test load.

3.2.3 Input voltage range inputvoltage range Instrument can correctly identify and capture the input signal voltage range.

3.2.4 The maximum input voltage maximuminputvoltage Without damaging the instrument maximum input voltage. This voltage exceeds the instrument or probe may be damaged.

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