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**Terminology of packages for semiconductor inte-grated circuits
(English Translation)**

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Table of Contents

What this standard covers	1
Document data	2
Standards quoted by this document	3

What this standard covers

This standard specifies the basic terms in the semiconductor integrated circuit packaging manufacturing, engineering and product inspection and other aspects of use. This standard applies to the production, research, teaching, and trade and other aspects of the application associated with the semiconductor integrated circuit package.

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